

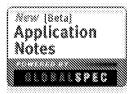
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		16-18 Sept. 2002 Page(s):334 - 339 Digital Object Identifier 10.1109/ICCD.2002.1106791			
		AbstractPlus Full Text: PDF(1339 KB) 1888 CNF			
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	2.	Direct addressed caches for reduced power consumption			
		Witchel, E.; Larsen, S.; Ananian, C.S.; Asanovic, K.; Microarchitecture, 2001, MICRO-34, Proceedings, 34th ACM/IEEE International Symposium on			
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		Digital Object Identifier 10.1109/MICRO.2001.991111			
		AbstractPlus Full Text: PDF(1094 KB) RESE ONE Rights and Permissions			
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		Abella, J.; Gonzalez, A.; Parallel and Distributed Processing Symposium, 2006, IPDPS 2006, 20th International			
		25-29 April 2006 Page(s):10 pp.			
		Digital Object Identifier 10.1109/IPDPS.2006.1639290			
		AbstractPlus Full Text: PDF(192 KB) HEEE ONF Rights and Permissions			
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		Vasic, D.; Costa, F.; Sarraute, E.; Power Electronics, IEEE Transactions on			
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Improving performance of large physically indexed caches by decoupling memory address

Rui Min; Yiming Hu;

Computers, IEEE Transactions on Volume 50, Issue 11, Nov. 2001 Page(s):1191 - 1201 Digital Object Identifier 10.1109/12.966494 AbstractPlus | References | Full Text: PDF(3100 KB) | HEEE J.M. Rights and Permissions CT image reconstruction using hexagonal grids ____ Knaup, M.; Steckmann, S.; Bockenbach, O.; Kachelriess, M.; Nuclear Science Symposium Conference Record, 2007, NSS '07, IEEE Volume 4, Oct. 26 2007-Nov. 3 2007 Page(s):3074 - 3076 Digital Object Identifier 10.1109/NSSMIC.2007.4436779 AbstractPlus | Full Text: PDF(611 KB) | IEEE ONF Rights and Permissions GraphStep: A System Architecture for Sparse-Graph Algorithms deLorimier, M.; Kapre, N.; Mehta, N.; Rizzo, D.; Eslick, I.; Rubin, R.; Uribe, T.E.; Knight, T.F.; DeF Field-Programmable Custom Computing Machines, 2006. FCCM '06, 14th Annual IEEE Symposis April 2006 Page(s):143 - 151 Digital Object Identifier 10.1109/FCCM.2006.45 AbstractPlus | Full Text: PDF(175 KB) | IEEE CNF Rights and Permissions Concurrent error detection/correction in the HAL MMU chip Chang, D.C.-W.; Saxena, N.R.; Fault-Tolerant Computing, 1993. FTCS-23. Digest of Papers.. The Twenty-Third International Syn 22-24 June 1993 Page(s):630 - 635 Digital Object Identifier 10.1109/FTCS.1993.627366 AbstractPlus | Full Text: PDF(560 KB) HESE ONE Rights and Permissions

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